



SM2246XT Flash F/W & ISP Release Information – O1027A

Introduction

This purpose of this document is to provide release information on the SM22X family F/W and ISP release information

Fix Coverage

- ☒ stands for the “new fix” or “new support” in the category
☐ stands for the “no-update” in the category

<input checked="" type="checkbox"/> MP Tool	<input checked="" type="checkbox"/> Controller ISP
<input type="checkbox"/> Yield Issue <input type="checkbox"/> Flash Issue <input type="checkbox"/> SLC Flash <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input type="checkbox"/> MLC Flash <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input type="checkbox"/> MP Tool Bug Fix <input checked="" type="checkbox"/> MP Tool New Function <input type="checkbox"/> Feature Enhance	<input type="checkbox"/> Yield Issue <input checked="" type="checkbox"/> Flash Issue <input type="checkbox"/> SLC Flash <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input checked="" type="checkbox"/> MLC Flash <input type="checkbox"/> Samsung Flash <input checked="" type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input checked="" type="checkbox"/> ISP Bug Fix <input type="checkbox"/> Feature Enhance



ISP Revision History

Version	MP Tool version	ISP version	Note
O1027A	O1102A	O1027A	Firmware 1. Support SNDK 1znm flash. 2. Fix 2ce 4Die inter intlv condition.
O1013A	O1022A	O1013A	Pretest 1. Modify Flow for CMDQ hang on 2. Add 2ce 4Die inter intlv condition 3. Support 4C4W Firmware 3. Support TSB 15nm flash. 4. Support SNDK A19nm 4-plan flash. 5. Modify Trim flow 6. Fix the bug of refresh H2f table 7. Fix 1Ch 1Way bug RDT 1. Modify RDT test scenario similar to SM2246EN.
O0818A	O0825A	O0818A	Pretest: 1. Pretest modification for too much bad CH/CE information Firmware: 1. Support L95B and L85A internal interleave. 2. SNDK 19nm and A19nm support enhancement 3. Toshiba 32nm
O0731A	O0810A	O0731A	Pretest: 1. Add CH and CE information when too many bad blocks error happened. Firmware: 1. Support L74A, L84C and TSB 9D2J internal interleave. 2. Improve H2F error handling RDT: 1. Add internal interleave define. 2. Change program multi plane behavior for internal interleave.
O0529A	O0609A	O0529A	1. Support Hynix 16nm MLC, 2. Fix small capacity burn-in issue 3. Improve GC efficiency 4. Pretest improvement



O0213E	O0311A	O0213E	<ol style="list-style-type: none">1. Add K9GCGD8U0A support2. Improve SPOR stability3. Improve HD tune read performance4. Fix Pretest0 bug5. Improve RDT
O0121A	O0107B	O0121A	<p>Firmware</p> <ol style="list-style-type: none">1. Fix SPOR mark bad issue2. Fix the issue of run out of spare block when smart read data <p>Pretest</p> <ol style="list-style-type: none">1. Add reference run time bad block function
N1215A	N1215A	N1215A	<ol style="list-style-type: none">1. Enhanced Page Mode First Release2. Flash Support List: Micron L95B, Micron L85A, Intel L85A

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

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